

Fig. 1

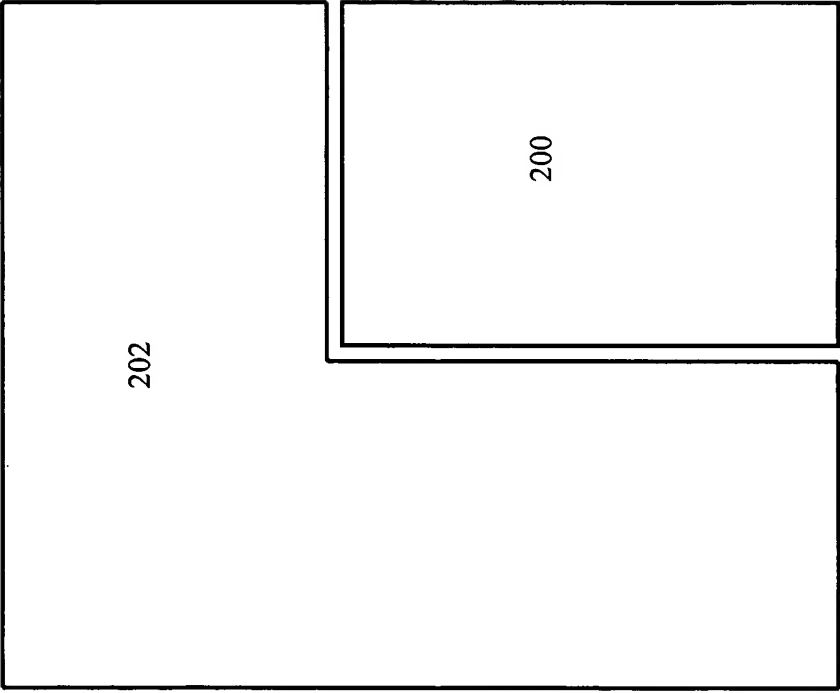


Fig. 2

Pin-No	1	2	3	4	5	6	7	8	9	Function
Test-Name										
Pin-Function	Input	I/O	I/O	I/O	Power	Output	Output	Input	Input	Power
Pin-Name	A1	Xtal1	Xtal0	Gnd	D0	Vout	Vin+	Vin-	Vcc	
Continuity	Test	Test	Ref	Test	Test	NC	NC	Ref	Continuity-Test (Limit)	
Idd				GND				Measure	Measure_Idd (Limit)	

Fig. 5

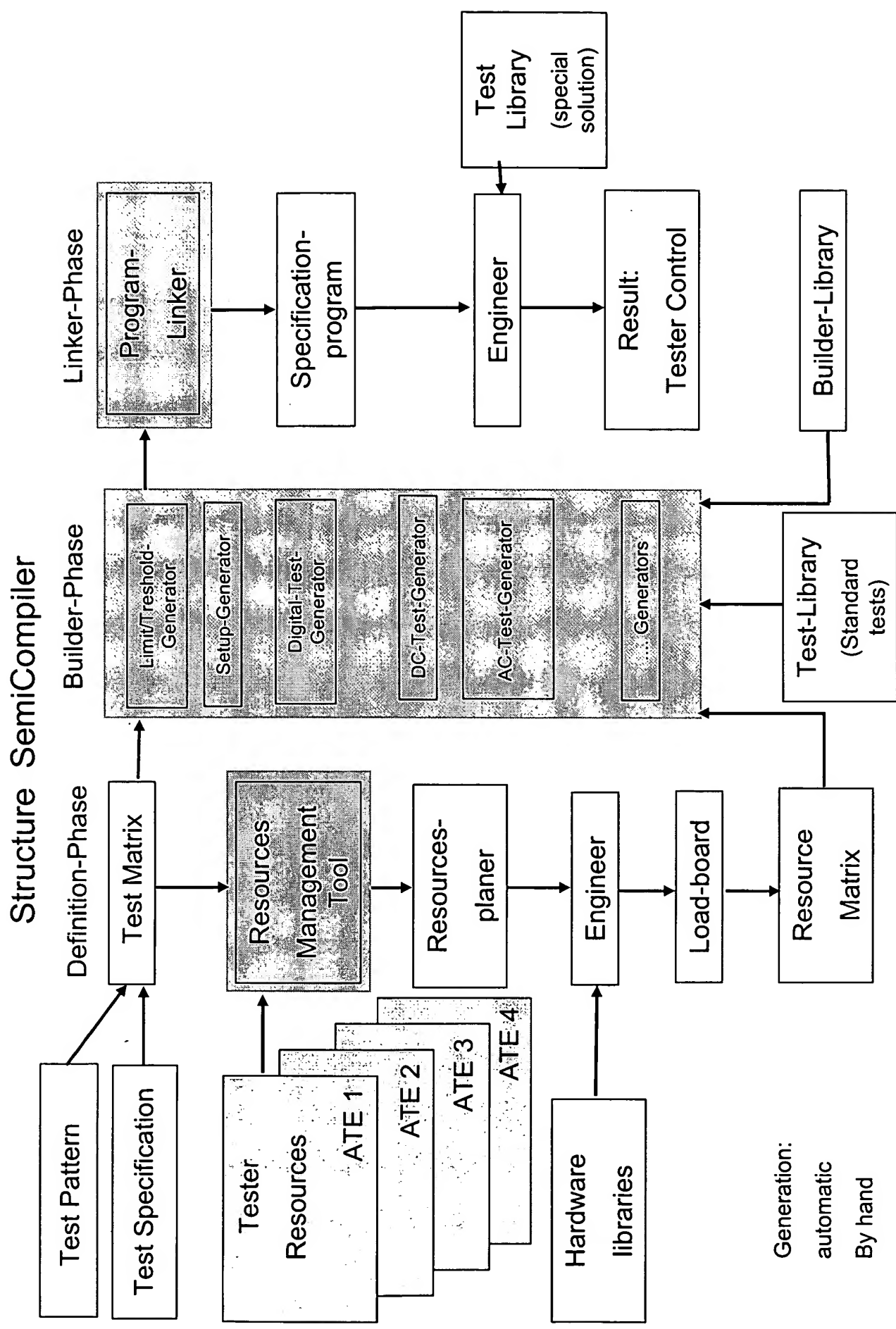


Fig. 6

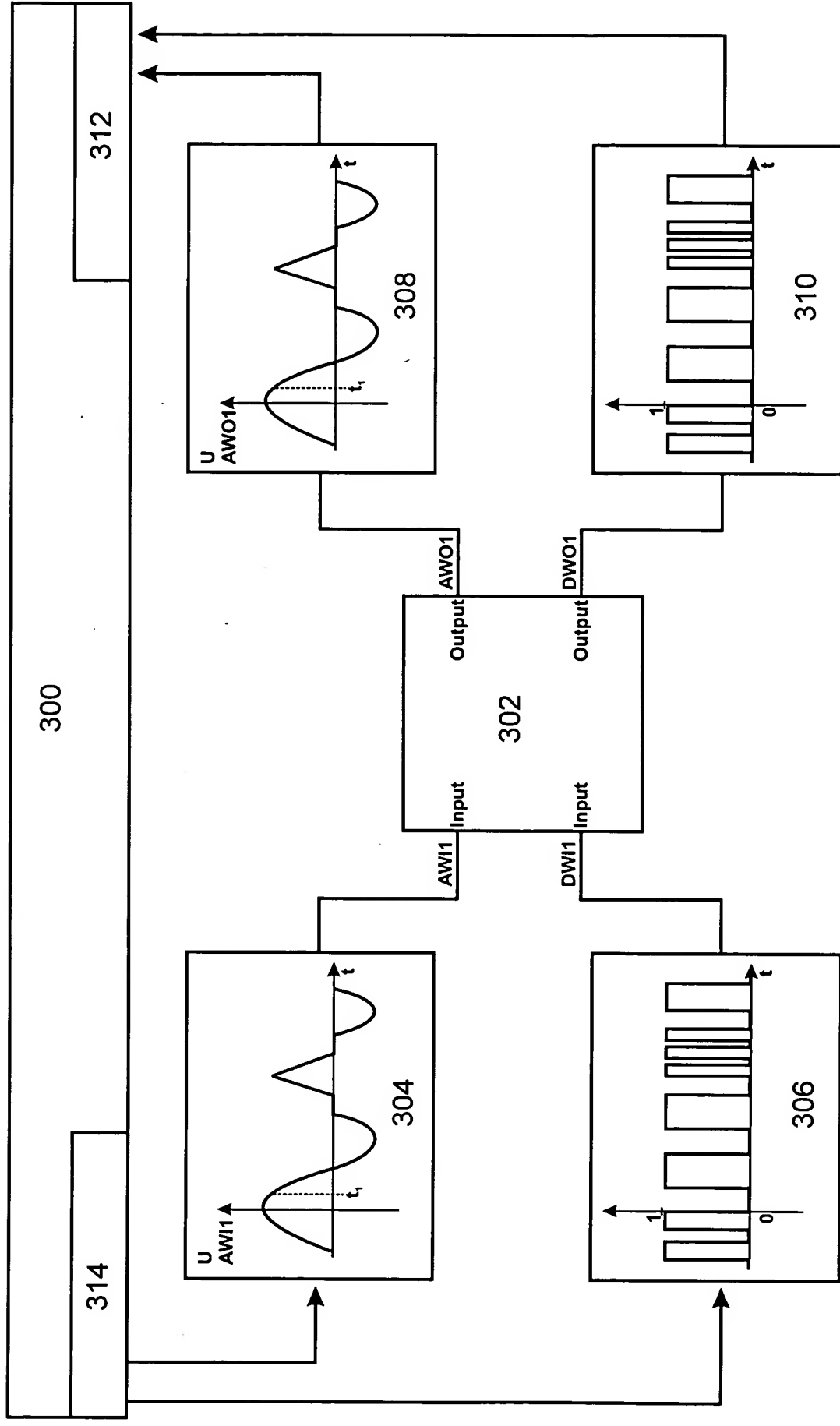


Fig. 7